



Avago Technologies Delivers 28G VSR-Compliant ASIC SerDes Geared for Faster, Lower-Power Networking Equipment

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Market Leader Surpasses 200 Million Embedded SerDes Channels Shipped

SAN JOSE, Calif. & SINGAPORE--(BUSINESS WIRE)--Nov. 29, 2011-- Avago Technologies (Nasdaq: [AVGO](#)), a leading supplier of analog interface components for communications, industrial and consumer applications, today announced that its 28-Gbps Serializer/Deserializer (SerDes) core has demonstrated compliance with the Common Electrical Interface (CEI) standard for 28G Very Short Reach (VSR). Achieving CEI-28G-VSR compliance brings the high-bandwidth, low-power SerDes to a broader range of data networking applications.

The company also announced it has shipped over 200 million embedded SerDes channels integrated in Application-Specific Integrated Circuits (ASICs) used for data communication in networking, computing and storage applications. The CEI-28G-VSR compliance and the shipping milestone reflect the growing demand for increased bandwidth for servers, routers and other data center equipment.

"Data center equipment designers are focused on increasing bandwidth to meet the demands of cloud computing and virtualization, but power consumption is always a key as well," said Frank Ostojic, vice president and general manager of the ASIC Products Division at Avago. "With 28G VSR compliance, our SerDes cores can better help our customers address these challenges for their next-generation networking equipment, which in turn will help Avago to continue setting new SerDes capability milestones."

Avago Intellectual Property (IP) SerDes cores can be easily integrated due to their modular, multirate architecture, and Avago has integrated over 400 SerDes channels on a single ASIC. The Avago SerDes cores feature a unique decision feedback equalization (DFE) architecture, resulting in a number of key performance differentiators such as low overall power, best-in-class data latency, and best-in-class jitter and crosstalk tolerance.

Avago has an established history of delivering on-time, reliable, high-performance ASICs. Three decades of design experience, state-of-the-art hierarchical design methodology, and an IP portfolio covering multiple standards form the company's foundation for supplying complex ASICs to the wired communications market. The broad Avago SerDes portfolio supports a wide range of standards such as PCI Express, Fibre Channel, XAUI, CEI, 10GBASE-KR and SFI, providing the flexibility to address optical, copper and backplane applications.

About Avago Technologies

Avago Technologies is a leading supplier of analog interface components for communications, industrial and consumer applications. By leveraging its core competencies in III-V compound and silicon semiconductor design and processing, the company provides an extensive range of analog, mixed signal and optoelectronics components and subsystems to approximately 40,000 end customers. Backed by strong customer service support, the company's products serve four diverse end markets: wireless communications, wired infrastructure, industrial and automotive electronics, and consumer and computing peripherals. Avago has a global employee presence and heritage of technical innovation dating back nearly 50 years to its Hewlett-Packard roots. Information about Avago is available on the Web at www.avagotech.com.

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