



## Micron Technology Licenses Avago Technologies' 30G SerDes for Hybrid Memory Cube Devices

April 10, 2014

### High Performance, Low Power Core Supports Next Gen HMC Devices

SAN JOSE, Calif., and SINGAPORE, April 10, 2014 (GLOBE NEWSWIRE) -- Avago Technologies (Nasdaq:AVGO), a leading supplier of analog interface components for wireless, wireline, and industrial applications, today announced that Micron Technology, Inc. (Nasdaq:MU) has licensed Avago's 28nm Low Power 30Gbps Serializer/Deserializer (SerDes) IP for use in Micron's next generation Hybrid Memory Cube (HMC) devices.

"Avago is an established leader in the development and delivery of high-speed SerDes cores with an offering that is very well suited for integration into our HMC product line," said Tom Eby, vice president of Micron's Compute and Networking Business Unit. "Based on its advanced performance and low power, we are confident that our customers will benefit from the presence of the Avago SerDes on both sides of the memory channel."

"We are pleased that our SerDes technology plays a vital role in Micron's next generation HMC offering," said Frank Ostojic, vice president and general manager of the ASIC/ASSP Product Division at Avago. "We are excited about participating in the major shift in memory technology that HMC represents."

With over 350 million SerDes channels shipped, Avago has an established history of delivering reliable, high-performance cores that support a wide range of industry specifications such as PCI Express, Fibre Channel, XAUI, CEI, 10GBASE-KR, SFI, and IEEE 802.3ba. The SerDes cores feature unique decision feedback equalization (DFE), resulting in lower overall power usage. Additional key differentiators include best-in-class data latency, noise immunity, jitter, and crosstalk performance.

As an industry-recognized technological breakthrough, Micron [HMC](#) devices use advanced through-silicon vias (TSVs)—vertical conduits that electrically connect a stack of individual chips—to combine high-performance logic with Micron's state-of-the-art [DRAM](#). HMC has been recognized by industry leaders and influencers as the long-awaited answer to the growing gap between the performance improvement rate of DRAM and processor data consumption rates. In 2013, Micron's HMC device was named Memory Product of the Year by EE Times, EDN and other leading electronics publications.

Information, technical specifications, tools and support for adopting HMC can be found at [www.hybridmemorycube.org](http://www.hybridmemorycube.org).

### About Avago Technologies

Avago Technologies is a leading designer, developer and global supplier of a broad range of analog, mixed signal and optoelectronics components and subsystems with a focus in III-V compound semiconductor design and processing. Backed by an extensive portfolio of intellectual property, Avago products serve three primary target markets: wireless communications, wired infrastructure, and industrial and other. Avago has a global employee presence and heritage of technical innovation dating back 50 years to its Hewlett-Packard roots. For more information, visit Avago's website: [www.avagotech.com](http://www.avagotech.com).

### About Micron

Micron Technology, Inc., is a global leader in advanced semiconductor systems. Micron's broad portfolio of high-performance memory technologies—including DRAM, NAND and NOR Flash is the basis for solid state drives, modules, multichip packages and other system solutions. Backed by more than 35 years of technology leadership, Micron's memory solutions enable the world's most innovative computing, consumer, enterprise storage, networking, mobile, embedded and automotive applications. Micron's common stock is traded on the NASDAQ under the MU symbol. To learn more about Micron Technology, Inc., visit [www.micron.com](http://www.micron.com).

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